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JOB OPENINGS FOR RDSS(研發替代役).

Analog Design Engineer [台北](#) [新竹](#)

- Master/PhD Degree in EE/CS or relevant
- Solid integrated circuit and solid state devices knowledge
- Familiar with circuit design EDA tools
- Knowledges in the following areas are a plus: PLL, high speed circuit design, I/O design, LVDS, ESD, ADC/DAC, switching regulator, low noise design

Digital Design Engineer [台北](#) [新竹](#)

- Master/PhD Degree in EE/CS or relevant
- Solid knowledge of semiconductor logic design and flow.
- Good understanding of video, computer and communications systems is a plus
- It is a plus to be familiar with DisplayPort, HDMI, USB and PC Express or other communication protocols

Digital Design Engineer--- Algorithm [台北](#) [新竹](#)

- Master/PhD Degree in EE/CS/Physics/Mathematics
- Good understanding of video and display
- Good at system modeling and simulation.
- Good understand image processing is a plus

Application Engineer ---Firmware [台北](#) [新竹](#)

- Master/PhD Degree, in EE/CS or relevant
- embedded microcontroller, embedded control application background
- Solid "C" and assembly language familiarity
- Knowledgeable on analog and MCU based application design

Software Engineer [台北](#) [新竹](#)

- Master Degree or above, Solid background in C and C++
- Familiar with embedded system, computer architecture, operating system
- Experience in Linux Kernel Driver (Display part preferred), Android System Infrastructure, Windows Driver.