



● **客戶支援類 Corporate Application Engineer (IC Validator)**
(in Taiwan, Hsinchu Science Park)

身份：應屆畢業生可

Key words: Laker/ DRC LVS/ Layout/ IC validation

Job Description:

1. Provides post-sales technical expertise during the installation, implementation, and maintenance of company products.
2. May be involved in implementing detailed customer installation requirements.
3. Ensures that client's needs have been met and that the product/solution is fully functioning according to specification.
4. May provide pre-sales support to company sales staff and customer personnel.

Job Requirement:

- Has a basic overall understanding of the design process. Strong communication skills are required.
- Has a strong understanding of ASIC design flow, VLSI, and/or CAD engineering.
- Knowledge of competitive EDA tool products and product knowledge in any of the areas of Verification, Place and Route, Design Reuse and/or Physical Design is preferred.



● **R&D Engineer, Physical Design (Hsinchu Science Park/ Taipei)**

身份：應屆畢業生可

Key words: Placement; PnR; Place and Route; ICC; IC Compiler

Job Description:

Responsible for designing, developing, troubleshooting, or debugging software programs. Develops software tools including operating systems, compilers, routers, networks, utilities, databases and internet-related tools, etc. Determines hardware compatibility and/or influences hardware design.

Job Requirement:

- Experience in programming software for operating systems, utilizing machine assembly and/or job control languages, and some knowledge of software capabilities.
- Designs algorithms and data structures. Experience on development of complex software projects, familiarity with C/C++ coding, and a strong background in data structures and algorithms.
- Has strong desires to learn and explore new technologies and demonstrates good analysis and problem-solving skills. Prior knowledge and experience of CAD tool development are preferred.



● **Product Validation Engineer (Hsinchu Chubei)**

身份：應屆畢業生可

Key words: Back-end EDA tools/ validation

Job Description:

- Responsible for validating the industry leading SOC implementation solutions - IC Compiler I and IC Compiler II.
- Setup, maintain and run full flow “place and route” customer designs and analyze deeply any Performance, Accuracy or Robustness issues found. Work with R&D on their resolution.
- Analyze specifications and requirements and identify potential problems early in the product life cycle.
- Develop and execute test plans which validate the product specifications and requirements, analyze and document the test results and co-operate with R&D Team to track down and resolve the issues.
- Define and create test structures and test designs for performance, accuracy and functional tests.
- Verify if product Defects and Enhancements have been fixed by R&D Team as desired and then check the test cases into product Quality Assurance Regression repository.
- Design and develop test programs in Perl, TCL and C/C++, including test tools and automated test suites

Job Requirement:

- Programming language (C/C++, Shell, TCL, Perl, Python etc.)
- Unix/Linux operating system
- Knowledge and experience in IC design (Verilog/VHDL, Synthesis, Place & Route etc.) and software engineering is a strong plus

Apply the position to join Synopsys: hyen@synopsys.com

Provide your English/ Chinese resume & Master & Bachelor transcripts

Contact window: Joyce (staffing coordinator)

<https://www.synopsys.com/>