專 題 演 講 大 綱

演 講 者：金仲達 教授

(國立清華大學資訊工程學系)

演講題目：Application-Aware Design and Evaluation of Network-on-Chip for Many-Core Systems(應用導向的多核心系統晶片網路之設計與評估)

演講摘要：As the number of cores in a chip multiprocessor (CMP) increases, the network-on-chip (NoC) becomes critical in sustaining the capabilities offered by the many cores. To optimize the design of NoC, it is necessary to understand the behavior of the workloads and use such information to guide the design. In this talk, we will examine a number of NoC designs that leverage the behavior of the workloads and discuss how the designs can be evaluated using trace-based simulations.